

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

the Application of:

BRIAN DOYLE, ET AL.

Application No.: 10/816,232

Filed: March 31, 2004

For: SEMICONDUCTOR DEVICE HAVING A Laterally Modulated Gate
Workfunction and Method of Fabrication

Art Group: 2815

Examiner: Edward Wojciechowicz

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
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Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Request for Continued Examination. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

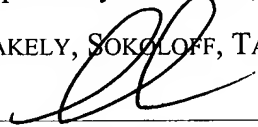
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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


Date: August 25, 2006



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Julie Dussault

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(use as many sheets as necessary)

Application Number	10/816,232
Filing Date	March 31, 2004
First Named Inventor	Brian Doyle
Art Unit	2815
Examiner Name	Edward Wojciechowicz
Attorney Docket Number	42P17292

of

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Date	
Considered	

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Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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